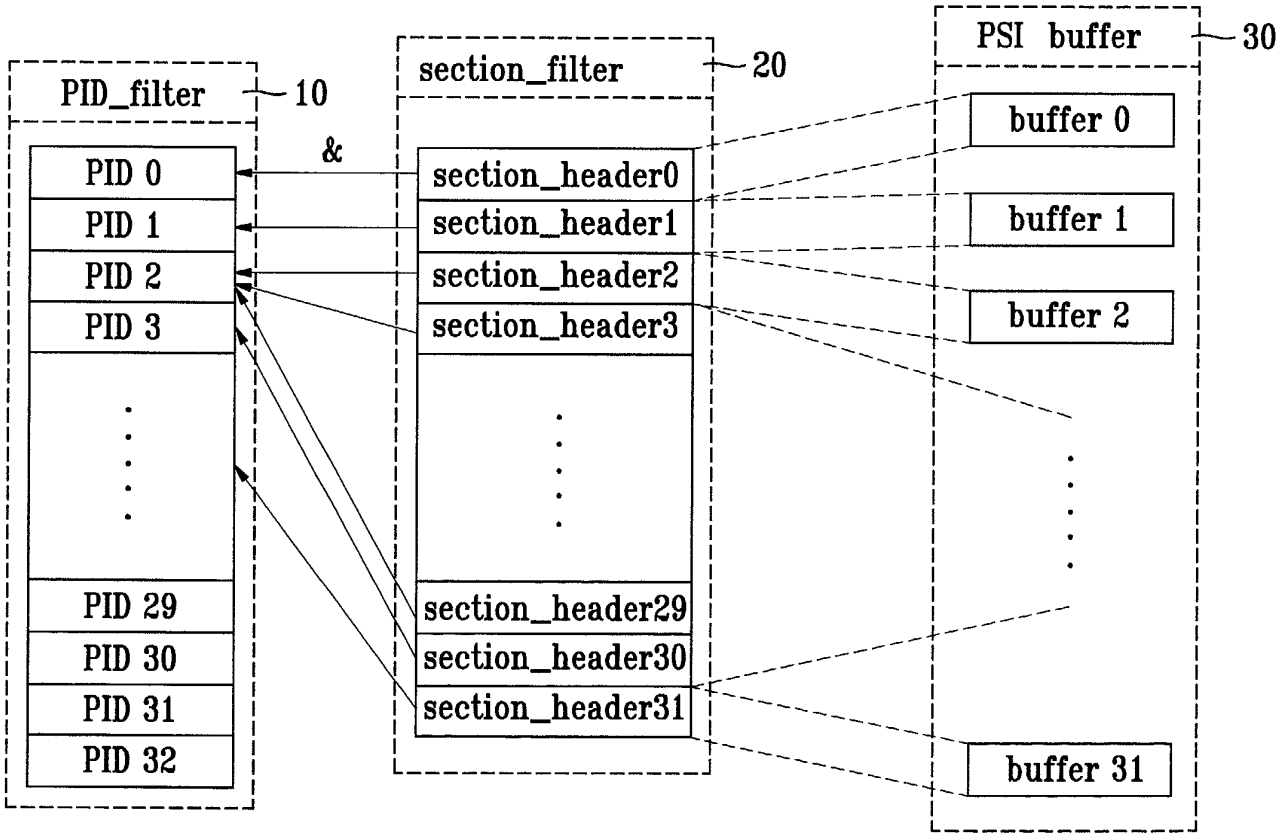
FIG.1
Related Art



DOCUMENT CLASSIFIED

FIG.2

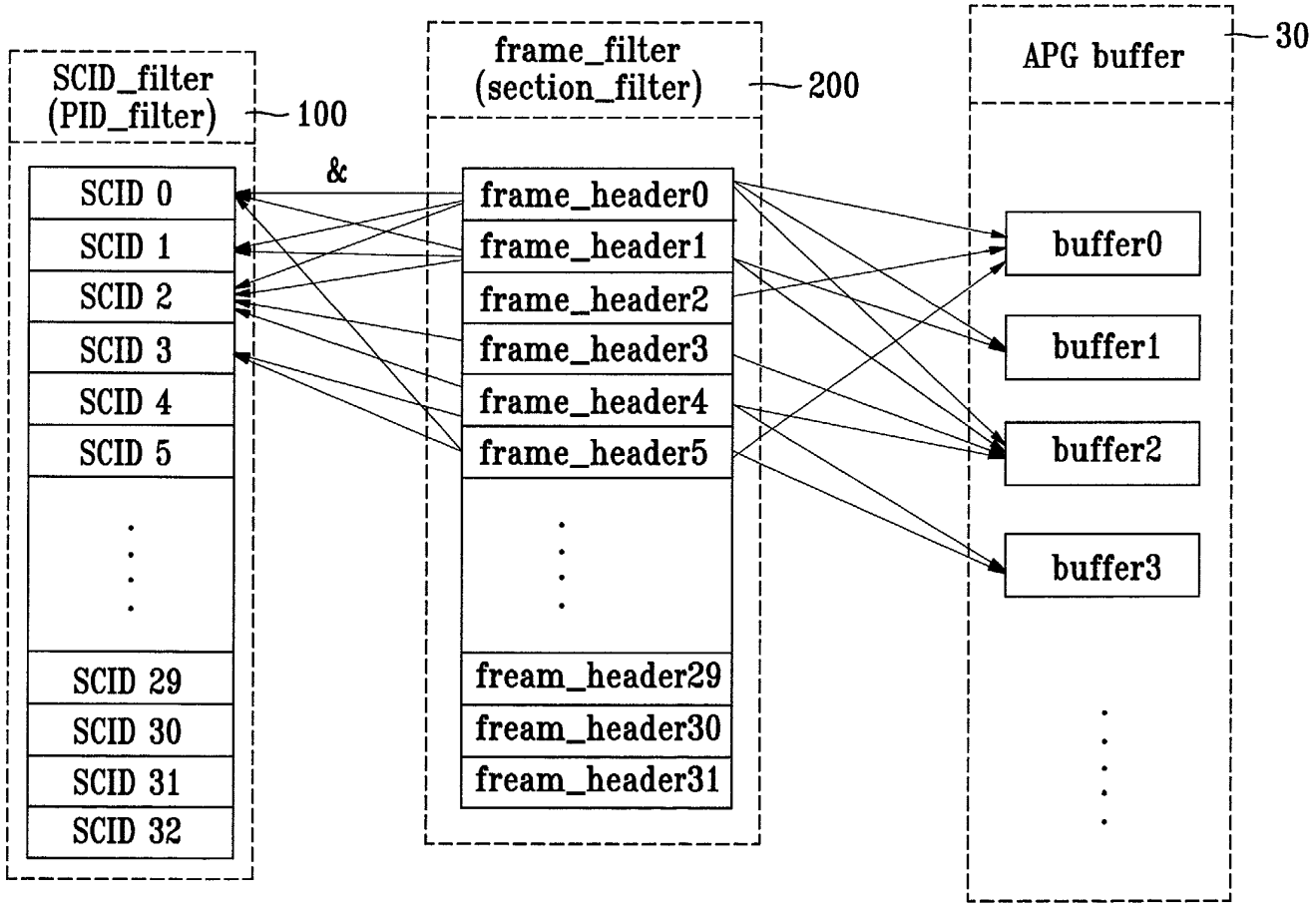


FIG.3

frema_header 0	SCID_number (5 bits) ex)0×0	SCID_number (5 bits) ex)0×1	SCID_number (5 bits) ex)0×2	SCID_number mask_bits (3 bits)
frema_header 1	SCID_number (5 bits) ex)0×1	SCID_number (5 bits) ex)0×2	SCID_number (5 bits) ex)None	SCID_number mask_bits (3 bits)
frema_header 2	SCID_number (5 bits) ex)0×0	SCID_number (5 bits) ex)None	SCID_number (5 bits) ex)None	SCID_number mask_bits (3 bits)
frema_header 3	SCID_number (5 bits) ex)0×2	SCID_number (5 bits) ex)None	SCID_number (5 bits) ex)None	SCID_number mask_bits (3 bits)
frema_header 4	SCID_number (5 bits) ex)0×2	SCID_number (5 bits) ex)0×3	SCID_number (5 bits) ex)None	SCID_number mask_bits (3 bits)
⋮				
frema_header 30	SCID_number (5 bits) ex)0×7	SCID_number (5 bits) ex)0×15	SCID_number (5 bits) ex)None	SCID_number mask_bits (3 bits)
frema_header 31	SCID_number (5 bits) ex)0×14	SCID_number (5 bits) ex)0×15	SCID_number (5 bits) ex)0×1a	SCID_number mask_bits (3 bits)

FIG.4

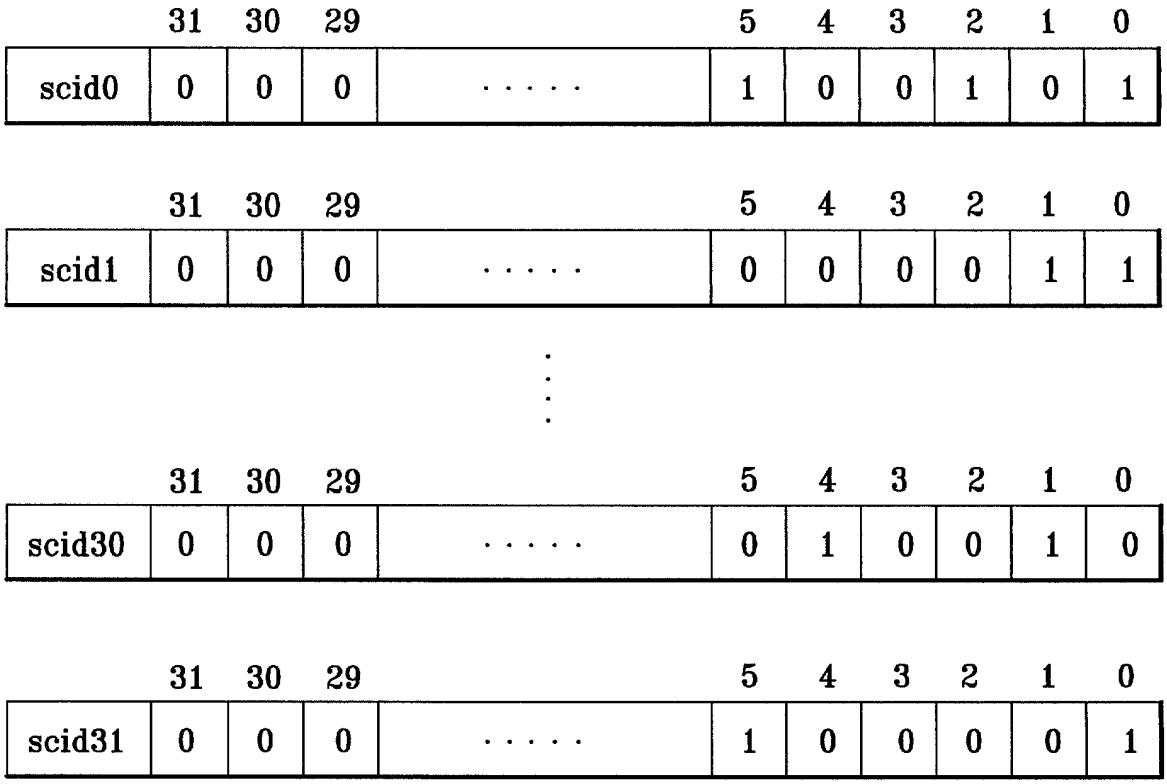


FIG.5

Frame_start_point (start address of buffer)	Frame_start_point (end address of buffer)	Matched SCID_number	Matched frame_header _number